Minutes for AMiBA Engineering Telecon

Meeting Date: 02-Jun-2005

Participants:
Australia:

USA: Patrick, M.T., Kyle, Johnson, Philippe

Taiwan: Chia-Hao, Homin, Po-I, Ted
USA Dial-in = 1-800-653-5390, 6668081#
Outside USA Dial-in = 1 773 843 6301

Minutes Recorder: Kyle

I.New Action Items:

II.Previous Action Items (still open):

III.Closed Action Items (as of this meeting):

IV.Miscellaneous Discussions:

Platform:

Philippe - Platform assembling was finished last Friday. Measurement of the pin position still needs to be done. Mr. Moo will come to see the platform next week. Philippe - The welding of interface ring is approximately half done. They claim it can be done at end of week. The schedule to install the platform early July is still holding.

M.T. - Measurement of the hole pattern and pin location is critical. Welcome everyone's input on how to measure about 2.5m distance at 1mm accuracy.

Philippe - Ted received a quote of the crane to be installed in the shelter. It can be retracted when the shelter is closed.

Mount:

Patrick - Vertex corrected the inconsistency in ObsPol value in this new version. Otherwise, it is the same with the previous one and seems to work. There is still safety issue which we wait for someone from Vertex to come and fix it. I have some questions about the pointing corrections in the documents and actually implemented and will discuss with Vertex. The anchor ring correction (total 8 deg) is also correctly implemented when I tried to do pointing. Tracking of stars looks ok.

 ${f Patrick}$ - I need to analyze the 24 stars pointing data and feedback the error to ${f Vertex.}$

M.T. - Klaus will come to Hilo around Jun/12 and leave Jun/16.

Receiver:

Receivers are shutdown due to moving. No further update.

Pierre - I think we still need to put some diode to protect the LNA. The voltage divider inside the LNA does not protect it from a spike. I'd like to talk to Johnson when he is down in Hilo.

Cold Head power distribution:

M.T. - Pierre's parts arrived today.

Correlator:

No updata.

C.T. - The problem I found is the clock signals distributed on the backplane are
distorted and not synchronized. I reroute the signal paths to synchronize them in FPGA.
Homin - You can add some buffer to reshape the waveform.

C.T.-I think I can also add some boost before distributing the signal. So I have to redesign the backplane in Taipei. Also I probably will setup a correlator PC in Taipei to speed up the test.

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Calibration System:

No updata.

LO/IF:

Homin - New guy (Skockly Yeh) is looking at documents and Johnson can help him on what to look.

 $\frac{\text{\underline{Misc:}}}{\text{\underline{M.T.}}} - \text{\underline{We got keys to the new office last Friday and we started to move. It's almost finished now but we still need furniture, electricity, air conditioner}$ and etc.